

Fiber Optical Modulator DP-QPSK (I-Q)

1530-1610 nm, 30GHz, 128Gbps, Push-Pull



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Features

- Low Drive Voltage, RF V_{π} Voltage 3.5V@32GBaud
- 128Gbps (32GBaud) Mach-Zehnder Modulator for DP-QPSK Transmission Format
- Separated DC Bias Port for Each Mach-Zehnder Interferometer
- Dual QPSK modulators and Polarization Beam Combiner are Integrated in One Package
- Integrated Monitor Photodiode for Auto Bias Control
- Low Insertion Loss, ≤ 12 dB

Applications

- DP-QPSK 128Gps Optical Transmission System

Note: The specifications provided are for general applications with a cost-effective approach. If you need to narrow or expand the tolerance, coverage, limit, or qualifications, please [click this link](#):



The DPSK-series fiber-optic modulator has an analog bandwidth 30GHz that supports 128 Gbps digital transmission using the DP-QPSK format, integrating four independently driven Mach-Zehnder interferometers (each offering ~30 GHz bandwidth, a low 3.5 V V_{π} , dedicated DC-bias ports, and monitor photodiodes for automatic bias control) in a single package. Two MZI pairs form dual I/Q modulators for the X- and Y-polarization paths, where the two arms correspond to the in-phase (I) and quadrature (Q) electrical inputs, enabling full complex modulation. A single-polarization CW laser is split into two orthogonal polarization channels, modulated independently, and recombined to produce a dual-polarization output. The design achieves 128 Gbps by combining a 30-Gbaud symbol rate with QPSK modulation (2 bits/symbol) and dual polarization, effectively utilizing all four MZIs to quadruple the data rate compared to a single-polarization modulator. The device provides ≤ 12 dB insertion loss and supports the full C+L band. Matching dual-channel RF amplifiers and an automatic bias controller are also available to simplify system integration. The device is based on traveling push-pull waveguide electrode design. This version has 4 RF inputs each MZI has $\pm$ push-pull electrodes. We offer mounting the device on an automatic bias control PCB option for convenient application.

Warning: The photodetector pins are highly sensitive to electrostatic discharge (ESD). Extreme caution must be exercised during handling to prevent permanent damage.

Note: Without bias controller, the device will not function properly.

Note: This component is intended for OEM system integration and is not designed for standalone laboratory use. Units supplied without a protective benchtop enclosure are provided without warranty, as they are susceptible to damage from improper handling, installation, or operation.

Specifications *

Parameter	Min	Typical	Max	Unit
Operating Wavelength	1530		1610	nm
Optical Bandwidth (-3dB Down)	23	28	35	GHz
Insertion Loss (for each polarization)			14	dB
RF Drive Voltage (2Vp @20GHz)			7.5	Vpp
Bias Half Wave Voltage		12	14	V
Monitor PD Responsivity			0.001	A/W
RF Impedance		50		ohm
DC Extinction Ratio for Parent MZI ^[1]	22			dB
DC Extinction Ratio for Child MZI ^[1]	20			dB
Optical Delay (between X and Y)	*1	-6	-1	ps
	*2	13.5	18.5	
Operating Temperature	-5		75	°C
Package Size (L x W x H)	79.4 x 13.2 x 7.0			mm

Notes:

[1]. With bias control

Note: Applying over the maximum RF control voltage or optical power will burn the device. Only the protected benchtop package has warranty.

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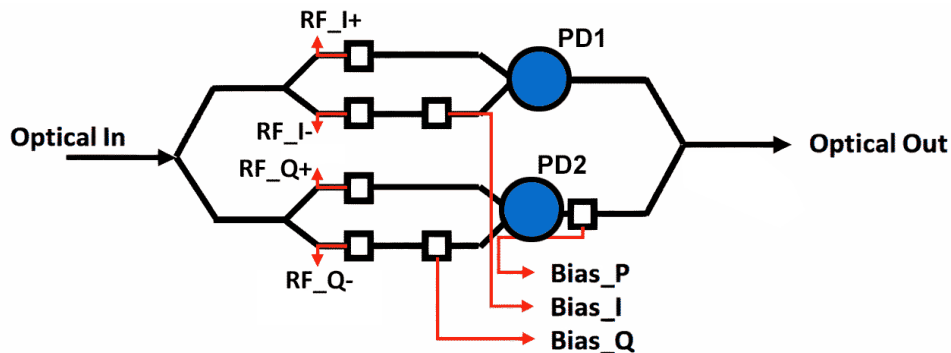
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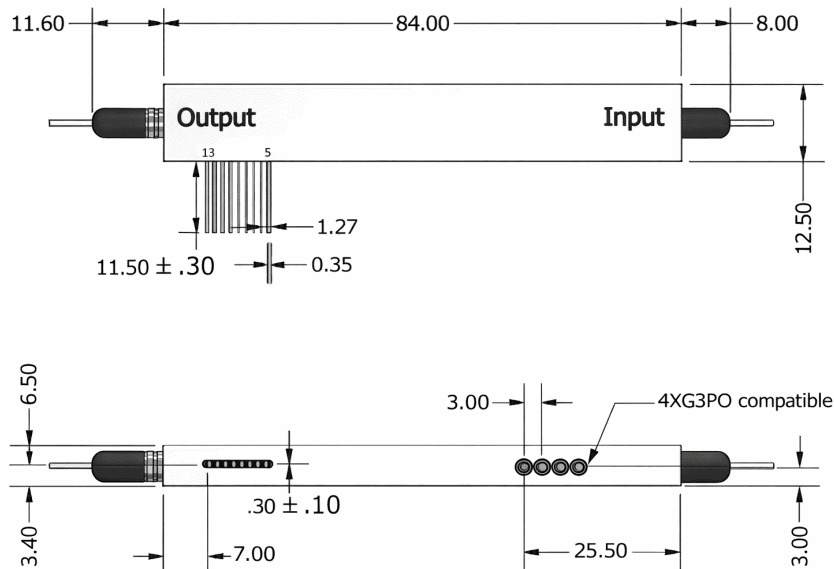


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Functional Diagram



Mechanical Dimensions (mm)



*Product dimensions may change without notice. This is sometimes required for non-standard specifications.

Pin Definition

Pin No.	Symbol	Function
1	PD1-A	Monitor Photodiode 1 Anode (I-arm)
2	PD1-K	Monitor Photodiode 1 Cathode (Common)
3	V-bias I	DC Bias for I-channel Child MZI
4	V-bias Q	DC Bias for Q-channel Child MZI
5	V-bias P	DC Bias for Parent (Phase) MZI
6	GND	Case Ground
7	PD2-A	Monitor Photodiode 2 Anode (Q-arm)
8	PD2-K	Monitor Photodiode 2 Cathode (Common)

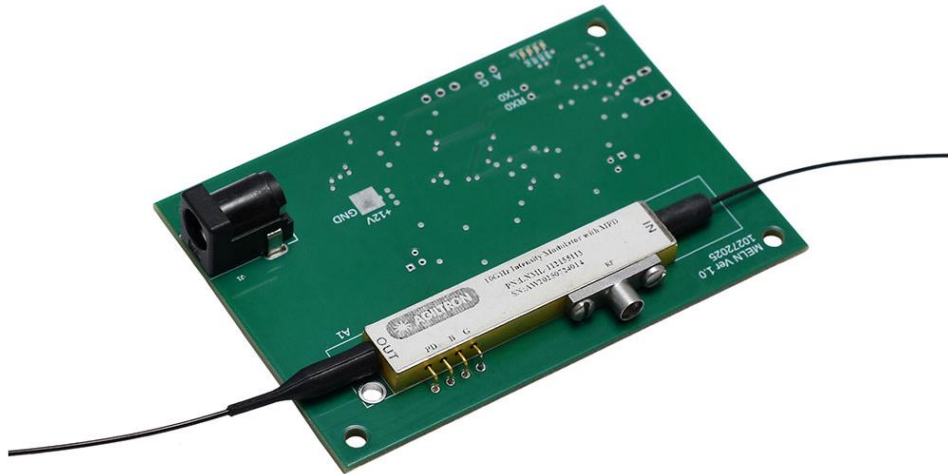
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Device Mounted On Automatic Bias Controller



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Ordering Information (Part Number)

	5	☐	☐	☐	5	5	☐	☐
Prefix	Wavelength	Auto Bias Controller	Connector ^[1]	RF Amplifier	Input Fiber	Output Fiber	Benchtop ^[2]	RF Connector
DPSP-	1550 nm = 5	Non = 1 Yes = 2 Special = 0	Non = 1 FC/APC = 2	Non = 1 Yes = 2	PM1550 = 5	PM1550 = 5	None = 1 Yes = 2	GPPO = 1 SMA = 2

[1]. The default connector configuration uses fiber with 0.9 mm buffer protection. The connector cannot be installed directly onto bare fiber because the bare fiber is prone to damage during shipping. However, the connector can be assembled on bare fiber if a 3 cm protective loose tube is added for reinforcement. The customer may remove this protective tube after testing. The optical power handling of a standard connector is less than 0.5 W for SMF-28 fiber and decreases further for smaller-core fibers.

[2]. Turn-key package integrates two differential RF linear amplifiers, auto bias controller, and power supply.

Application Notes

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Instruction for Comprehensive Characterization Procedure for LiNbO₃ Modulator

1. Initial Setup and Inspection

- **Visual inspection:** Check for fiber integrity, waveguide cleanliness, electrode contacts.
- **Connector integrity:** Inspect RF and optical connectors for damage or debris.
- **Polish & clean fibers:** Use fiber scope and fiber wipes if necessary.
- **Thermal stabilization:** Place the device on a temperature-controlled stage (if needed).

2. Electrical & RF Interface Check

- Measure **RF return loss (S11)** using a VNA.
- Confirm **50 Ω impedance matching** across the modulator's bandwidth.
- Verify the **bias-T (if present)** does not short or distort the signal.
- Check for **electrode continuity** and isolation between electrodes.

3. Optical Characterization

a. Insertion Loss

- Use a narrow-linewidth laser (e.g., at 1550 nm).
- Launch light into input fiber and measure output power.
- Insertion Loss = $10 \cdot \log(P_{in} / P_{out})$
(Typical LiNbO₃ insertion loss: 3–7 dB).

b. Extinction Ratio (for MZIs)

- Apply DC voltage sweep to modulation electrodes.
- Record maximum and minimum output powers.
- ER = $10 \cdot \log(P_{max} / P_{min})$
(Good ER: >20 dB for analog, >30 dB for digital).

c. V π Measurement (Half-wave voltage)

- Apply a slowly varying triangular or sinusoidal voltage.
- Measure output optical power vs. voltage.
- Determine V π = voltage required to induce a π phase shift (first null).
- Also check **V π ·L** product (performance metric).

4. Electrical & RF Interface Check

- Connect modulator RF input to a signal generator or VNA (S21 test).
- Bias the MZI at quadrature (linear point of transfer curve).
- Apply RF sweep from 100 kHz to >20 GHz (or device limit).
- Use a fast photodiode and spectrum analyzer or VNA.
- Record **modulation response (S21)**:
 - Bandwidth = -3 dB point from peak response.
 - Watch for peaking or drop-offs due to impedance mismatch or piezoelectric resonances.

5. Linearity Tests (for Analog Applications)

- Use two-tone RF input (e.g., f1 = 10 GHz, f2 = 10.01 GHz).
- Measure intermodulation distortion (IMD3).
- Calculate **Spurious-Free Dynamic Range (SFDR)**.

6. Bias Drift / Stability Testing

- Apply DC bias and monitor optical output over time.
- Check for **DC drift**, typically caused by photorefractive or charge trapping effects.
- Test under light-on and light-off conditions.
- Optional: Integrate **auto-bias control loop**.

7. Thermal Response (Optional)

- Vary modulator temperature (e.g., from 20°C to 70°C).
- Track **V π , insertion loss, and ER** as a function of temperature.
- Identify thermal sensitivity and hysteresis.
- Important for packaged systems or field deployments.

8. High-Power Handling & Backreflection

- Gradually increase optical input power (with isolator).
- Watch for power-induced drift or photorefractive effects.
- Check backreflections using an **optical return loss meter (ORL)**.

9. Data Logging and Reporting

- Capture all test data in a structured format.
- Plot:
 - Optical power vs. voltage (transfer function)
 - S21 vs. frequency
 - V π vs. temperature
 - Drift vs. time
- Include tables for:
 - Insertion loss
 - ER
 - V π and bandwidth
 - Bias stability
 - RF return loss

Optional Advanced Tests:

- **Jitter analysis** (for digital modulators)
- **Bit Error Rate Testing (BERT)** for NRZ or PAM signals
- **Eye diagram measurements**
- **Pulse response and rise/fall time**
- **Photorefractive damage threshold test**

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RF Performance

